



Development of a low-power-signal acquisition device for signals lower than 25 Hz

By Franz Peter Zantis

GRIN Verlag Jul 2012, 2012. Taschenbuch. Book Condition: Neu. 211x146x10 mm. This item is printed on demand - Print on Demand Neuware - Diploma Thesis from the year 2012 in the subject Electrotechnology, grade: 2.0 bzw.'B', The Slovak Technical University (Faculty of electrical engineering and information technology), language: English, abstract: Design a signal detection device for detection and digitization of weak signals with frequencies up to 25 Hz before their transmission into a personal computer (PC). The device should contain an input low-pass filter with a cutoff frequency of 25 Hz, a preamplifier, microcontroller MSP430F2013 for digitization and preparation of the digitized signals for transmission into the PC via USB interface. 64 pp. Englisch.

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